



Fee purpose

PATENT
Conf. No. 6106

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Siuki Chan
Assignee: Xilinx, Inc.
Title: Methods and Circuits for Measuring Clock Skew on
Programmable Logic Devices
Serial No.: 10/021,447 Filing Date: 10/30/2001
Examiner: Aditya S. Bhat Art Unit: 2863
Docket No.: X-885 US Conf. No.: 6106

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AMENDMENT SUBMITTED PURSUANT TO
REQUEST FOR CONTINUED EXAMINATION

In response to the outstanding Office Action mailed from
the Patent Office on December 4, 2003 and pursuant to the
Request for Continued Examination filed herewith, Applicant
submits the following amendments and remarks.

Amendments to the Claims begins on page 2 of this paper.
Remarks begin on page 9 of this paper.

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